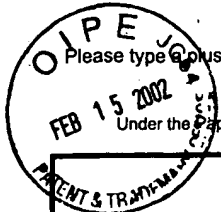


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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Applicati n Number	09/814,417	
	Filing Date	March 21, 2001	
	First Named Inventor	Subbarao, Prasad	
	Group Art Unit		
	Examiner Name		
Total Number of Pages in This Submission	4	Attorney Docket Number	00-077 68513

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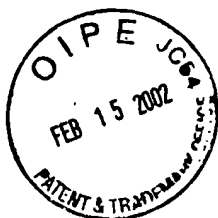
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Subbarao et al.
Serial No.: 09/814,417
Filed: March 21, 2001
For: DRIVER WAVEFORM
MODELING WITH
MULTIPLE EFFECTIVE
CAPACITANCES

Group Art
Unit:

Examiner(s):

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The filing of this Information Disclosure
Statement is being made before the mailing of a first
Office action on the merits of the above-identified patent
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In accordance with the duty of disclosure imposed
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representative that may be material to the examination of
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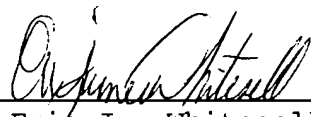
provides this Information Disclosure Statement in accordance with 37 C.F.R. 1.97-1.98 and includes Form PTO-1449 with one reference.

Applicants respectfully request that the Examiner review the foregoing reference and that it be made of record in the file history of the above-captioned application.

Although this document is being made known to the Patent and Trademark Office in compliance with Applicant's duty of disclosure, such disclosure is not to be construed as an admission by Applicants or Applicants' representative that the reference is effective as prior art against the subject application. In accordance with 37 C.F.R. 1.97(b), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 C.F.R. 1.56(a) exists.


Respectfully submitted,

February 4, 2002
Date

By: 
Eric J. Whitesell
Registration No. 38,657

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Milpitas, CA 95035
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Form PTO-1449 (Modified) INFORMATION DISCLOSURE CITATION IN AN APPLICATION 	Application No.	09/814,417
	Filing Date	March 21, 2001
	First Named Inventor	Subbarao, Prasad
	Group Art Unit	
	Examiner Name	
Sheet 1 of 1	Attorney Docket No.	00-077 68513

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	AA		FLORENTIN DARTU et al.; "Performance Computation for Precharacterized CMOS Gates with RC Loads"; IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 15, No. 5, May 1996; pp. 544-553.

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